

PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): En-Hsing Chen et al.

Title: NAND MEMORY ARRAY INCORPORATING MULTIPLE SERIES
SELECTION DEVICES AND METHOD FOR OPERATION OF SAME

Application No.: 10/729,865

Filed: December 5, 2003

Examiner: Nguyen, Van Thu T.

Group Art Unit: 2824

Atty. Docket No.: 023-0029

September 20, 2004

Mail Stop Amendment
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Alexandria, VA 22313-1450**INFORMATION DISCLOSURE STATEMENT**
37 C.F.R. § 1.97(b)

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- ☒ Form(s) PTO-1449 (1 pages), including copy(ies) of 2 reference(s).
☐ Other: n/a

to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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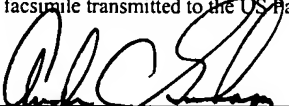
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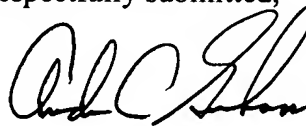
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- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
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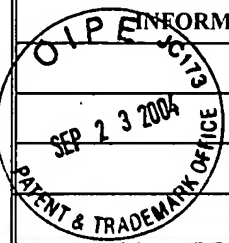
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 Andrew C. Graham	<u>9/20/04</u> Date

Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 023-0029		
				Application No.: 10/729,865		
 INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				Applicant(s): En-Hsing Chen et al.		
				Filing Date: December 5, 2003		
				Group Art Unit: 2824		
				Date Submitted: September 20, 2004		
U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
	AA	4,646,266	24 Feb 1987	Ovshinsky et al.		
	AB	5,514,907	07 May 1996	Moshayedi		
	AC	6,005,270	21 Dec 1999	Noguchi		
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	AI					
	AJ					
	AK					
Foreign Patent Documents						
					Translation	
		Document	Date	Country	Yes	No
	AL	0 528 367 A1	24 Feb 1993	Europe		
	AM	0 575 051 A1	22 Dec 1993	Europe		
	AN					
	AP					
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)						
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